



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: 2123)
)
Examiner: J. Proctor)
)
Applicant(s): J. G. Walacavage et al.)
)
Serial No.: 09/965,904)
)
Filing Date: September 28, 2001)
)
For: METHOD OF LOGICAL MODELING OF)
OPERATOR INTERACTION WITH)
PROGRAMMABLE LOGIC CONTROLLER)
LOGICAL VERIFICATION SYSTEM)

AMENDMENT
UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

This is in response to the Office Action dated February 14, 2007, regarding the
above-identified application.

ok to
Enter
JUP 4/30/07

CERTIFICATE OF MAILING: (37 C.F.R. 1.8) I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the U.S. Postal Service with sufficient postage as First Class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on April 16, 2007, by Daniel H. Bliss
Daniel H. Bliss